

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	09994600
	Filing Date	2001-11-19
	First Named Inventor	Douglas H. Anderson
	Art Unit	2178
	Examiner Name	Kyle R. Stork
	Attorney Docket Number	CD01174M

U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6910126	B1	2005-06-21	Mar et al.	Entire Document
	2	6825689	B1	2004-11-30	Snyder	Entire Document
	3	6798299	B1	2004-09-28	Mar et al.	Entire Document
	4	7023257	B1	2006-04-04	Sullam	Entire Document
	5	6967511	B1	2005-11-22	Sullam	Entire Document
	6	6892322	B1	2005-05-10	Snyder	Entire Document
	7	6823282	B1	2004-11-23	Snyder	Entire Document
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9	6611220	B1	2003-08-26	Snyder	Entire Document
10	7180342	B1	2007-02-20	Shutt et al.	Entire Document
11	6765407	B1	2004-07-20	Snyder	Entire Document
12	7386740	B1	2008-06-10	Kutz et al.	Entire Document
13	6952778	B1	2005-10-04	Snyder	Entire Document
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21	6603330	B1	2003-08-05	Snyder	Entire Document
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23	7005933	B1	2006-02-28	Shutt	Entire Document
24	6542025	B1	2003-04-01	Kutz et al.	Entire Document
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31	6563391	B1	2003-05-13	Mar	Entire Document
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8	USPTO Notice of Allowance for 10/033,027 (CD00232) dated 03/31/09; 7 pages	<input type="checkbox"/>
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11	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 12/21/06; 31 pages	<input type="checkbox"/>
12	USPTO Final Rejection for 10/033,027 (CD00232) dated 08/09/06; 6 pages	<input type="checkbox"/>
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17	Application Number 09/964,991 (CD00196): "A Novel Band-Gap Circuit for Providing an Accurate Reference Voltage Compensated for Process State, Process Variations and Temperature," Kutz et al.; 25 pages	<input type="checkbox"/>
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19	"An Analog PPL-Based Clock and Data Recovery Circuit with High Input Jitter Tolerance;" Sun, Reprinted from IEEE Journal of Solid-State Circuits, 1989; 4 pages	<input type="checkbox"/>
20	"WP 3.5: An Integrated Time Reference;" Blauschild, Digest of Technical Papers, 1994; 4 pages	<input type="checkbox"/>
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	2	6535946	B1	2003-03-18	Bryant et al.	Entire Document
	3	4827401	B1	1989-05-02	Hrustich et al.	Entire Document
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31	5428319	B1	1995-06-27	Marvin et al.	Entire Document
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33	5898345	B1	1999-04-27	Namura et al.	Entire Document
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46	5319370	B1	1994-06-07	Signore et al.	Entire Document
47	5440305	B1	1995-08-08	Signore et al.	Entire Document
48	5546433	B1	1996-08-13	Tran et al.	Entire Document
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	1	PCT/US99/09712	WO	A1	1999-02-25	Schutte	Entire Document	<input type="checkbox"/>
	2	PCT/US96/17305	WO	A1	1996-06-06	Schutte	Entire Document	<input type="checkbox"/>
	3	PCT/US98/34376	WO	A1	1998-08-06	Schutte	Entire Document	<input type="checkbox"/>

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	1	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/09/08; 34 pages	<input type="checkbox"/>
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	3	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 01/07/08; 30 pages	<input type="checkbox"/>

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4	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 07/31/07; 28 pages	<input type="checkbox"/>
5	USPTO Final Rejection for 09/975,115 (CD00182) dated 02/21/07; 25 pages	<input type="checkbox"/>
6	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/31/06; 19 pages	<input type="checkbox"/>
7	USPTO Final Rejection for 09/975,115 (CD00182) dated 06/23/06; 20 pages	<input type="checkbox"/>
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11	USPTO Final Rejection for 09/975,338 (CD00186) dated 01/31/08; 21 pages	<input type="checkbox"/>
12	USPTO Non-Final Rejection for 09/975,338 (CD00186) dated 08/14/07; 19 pages	<input type="checkbox"/>
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15	USPTO Advisory Action for 09/975,338 (CD00186) dated 05/15/06; 4 pages	<input type="checkbox"/>
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19	Application Number 09/207,912 (CD98086): "Circuit(s), Architecture and Method(s) for Operating and/or Tuning a Ring Oscillator," Mar; 23 pages	<input type="checkbox"/>
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34	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module;" Perrin; 41 pages	<input type="checkbox"/>
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First Named Inventor	Douglas H. Anderson
Art Unit	2178
Examiner Name	Kyle R. Stork
Attorney Docket Number	CD01174M

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34	Application Number 09/887,923 (CD00229): "Novel Method and System for Interacting between a Processor and a Power on Reset to Dynamically Control Power States in a Microcontroller;" Kutz; 44 pages	<input type="checkbox"/>

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35	Application Number 10/000,383 (CD00189): "System and Method of Providing a Programmable Clock Architecture for an Advanced Microcontroller;" Sullam; 34 pages	<input type="checkbox"/>
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8	Hong et al.; "An FPGA-Based Hardware Emulator for Fast Fault Emulation"; IEEE 1997; 4 pages	<input type="checkbox"/>
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Examiner Name	Kyle R. Stork
Attorney Docket Number	CD01174M

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Filing Date	2001-11-19
First Named Inventor	Douglas H. Anderson
Art Unit	2178
Examiner Name	Kyle R. Stork
Attorney Docket Number	CD01174M

FOREIGN PATENT DOCUMENTS

Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ²	Kind Code ⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T ⁵
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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
	1	USPTO Notice of Allowance for Application Number 09/930,021 (CD00174) dated 11/26/2004; 4 pages	<input type="checkbox"/>
	2	USPTO Final Rejection for Application Number 09/930,021 (CD00174) dated 08/31/2004; 8 pages	<input type="checkbox"/>
	3	USPTO Non-Final Rejection for Application Number 09/930,021 (CD00174) dated 04/26/2004; 6 pages	<input type="checkbox"/>
	4	USPTO Miscellaneous Action with SSP for Application Number 09/930,021 (CD00174) dated 10/01/2001; 1 page	<input type="checkbox"/>
	5	USPTO Notice of Allowance for Application Number 09/953,423 (CD00187) dated 07/12/2004; 6 pages	<input type="checkbox"/>
	6	USPTO Non-Final Rejection for Application Number 09/953,423 (CD00187) dated 02/06/2004; 5 pages	<input type="checkbox"/>
	7	USPTO Notice of Allowance for Application Number 09/957,084 (CD00201) dated 05/18/2004; 5 pages	<input type="checkbox"/>

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Attorney Docket Number	CD01174M

8	USPTO Final Rejection for Application Number 09/957,084 (CD00201) dated 01/29/2004; 8 pages	<input type="checkbox"/>
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11	USPTO Non-Final Rejection for Application Number 09/957,084 (CD00201) dated 08/23/2002; 6 pages	<input type="checkbox"/>
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15	USPTO Notice of Allowance for Application Number 09/969,311 (CD00175) dated 03/01/2005; 4 pages	<input type="checkbox"/>
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17	USPTO Advisory Action for Application Number 09/969,311 (CD00175) dated 07/21/2003; 2 pages	<input type="checkbox"/>
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23	USPTO Requirement for Restriction/Election for Application Number 09/972,003 (CD00176) dated 05/06/2004; 4 pages	<input type="checkbox"/>
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27	USPTO Final Rejection for Application Number 09/972,133 (CD00178) dated 03/30/2006; 8 pages	<input type="checkbox"/>
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33	USPTO Non-Final Rejection for Application Number 09/975,104 (CD00183) dated 06/16/2006; 8 pages	<input type="checkbox"/>
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35	USPTO Non-Final Rejection for Application Number 09/975,104 (CD00183) dated 08/16/2005; 6 pages	<input type="checkbox"/>
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37	USPTO Notice of Allowance for Application Number 09/975,030 (CD00185) dated 02/06/2006; 7 pages	<input type="checkbox"/>
38	USPTO Non-Final Rejection for Application Number 09/975,030 (CD00185) dated 10/20/2005; 7 pages	<input type="checkbox"/>
39	USPTO Non-Final Rejection for Application Number 09/975,030 (CD00185) dated 03/29/2005; 13 pages	<input type="checkbox"/>
40	USPTO Notice of Allowance for Application Number 09/977,111 (CD00202) dated 09/28/2006; 6 pages	<input type="checkbox"/>

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41	USPTO Notice of Allowance for Application Number 10/272,231 (CD00170C) dated 03/08/2004; 6 pages	<input type="checkbox"/>
42	USPTO Final Rejection for Application Number 10/272,231 (CD00170C) dated 11/05/2003; 5 pages	<input type="checkbox"/>
43	USPTO Non-Final Rejection for Application Number 10/272,231 (CD00170C) dated 07/14/2003; 6 pages	<input type="checkbox"/>
44	USPTO Notice of Allowance for Application Number 11/125,554 (CD00230C) dated 02/07/2008; 4 pages	<input type="checkbox"/>
45	USPTO Notice of Allowance for Application Number 11/125,554 (CD00230C) dated 04/24/2007; 7 pages	<input type="checkbox"/>
46	USPTO Non-Final Rejection for Application Number 11/125,554 (CD00230C) dated 12/11/2006; 9 pages	<input type="checkbox"/>
47	USPTO Notice of Allowance for Application Number 09/855,868 (CD00181) dated 04/25/2005; 6 pages	<input type="checkbox"/>
48	USPTO Non-Final Rejection for Application Number 09/855,868 (CD00181) dated 08/26/2004; 7 pages	<input type="checkbox"/>
49	USPTO Notice of Allowance for Application Number 09/887,923 (CD00229) dated 09/27/2004; 5 pages	<input type="checkbox"/>
50	USPTO Non-Final Rejection for Application Number 09/887,923 (CD00229) dated 05/25/2004; 7 pages	<input type="checkbox"/>

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Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (04-09)

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U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6934674	B1	2005-08-23	Douzy et al.	Entire Document
	2	6051772	B1	2000-04-18	Cameron et al.	Entire Document
	3	6016554	B1	2000-01-18	Skrovan et al.	Entire Document
	4	6704889	B1	2004-03-09	Veenstra et al.	Entire Document
	5	5978937	B1	1999-11-02	Miyamori et al.	Entire Document
	6	5317202	B1	1994-05-31	Waizman	Entire Document
	7	6121805	B1	2000-09-19	Thamsirianunt et al.	Entire Document
	8	6211708	B1	2001-04-03	Kemmer	Entire Document

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9	6396302	B1	2002-05-28	New et al.	Entire Document
10	6204687	B1	2001-03-20	Schutz et al.	Entire Document
11	5996032	B1	1999-11-30	Baker	Entire Document
12	5608892	B1	1997-03-04	Wakerly	Entire Document
13	5808883	B1	1998-09-15	Hawkes	Entire Document
14	5949632	B1	1999-09-07	Barreras Sr. et al.	Entire Document
15	6542025	B1	2003-04-01	Kutz et al.	Entire Document
16	5554951	B1	1996-09-10	Gough	Entire Document
17	5894243	B1	1999-04-13	Hwang	Entire Document
18	6081140	B1	2000-06-27	King	Entire Document
19	6104217	B1	2000-08-15	Magana	Entire Document

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20	6286127	B1	2001-09-04	King et al.	Entire Document
21	6166960	B1	2000-12-26	Marneweck et al.	Entire Document
22	5982105	B1	1999-11-09	Masters	Entire Document
23	4740966	B1	1988-04-26	Goad	Entire Document
24	6643151	B1	2003-11-04	Nebrigic et al.	Entire Document
25	6003133	B1	1999-12-14	Moughanni et al.	Entire Document
26	6339815	B1	2002-01-15	Feng et al.	Entire Document
27	6574739	B1	2003-06-03	Kung et al.	Entire Document
28	5737760	B1	1998-04-07	Grimmer et al.	Entire Document
29	5969632	B1	1999-10-19	Diamant et al.	Entire Document
30	6009496	B1	1999-12-28	Tsai	Entire Document

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31	6839774	B1	2005-01-04	Ahn et al.	Entire Document
32	6754765	B1	2004-06-22	Chang et al.	Entire Document
33	5630057	B1	1997-05-13	Hait	Entire Document
34	6324628	B1	2001-11-27	Chan	Entire Document
35	6236593	B1	2001-05-22	Hong et al.	Entire Document
36	6219729	B1	2001-04-17	Keats et al.	Entire Document
37	5319771	B1	1994-06-07	Takeda	Entire Document
38	5378935	B1	1995-01-03	Korhonen et al.	Entire Document
39	5572719	B1	1996-11-05	Biesterfeldt	Entire Document
40	5579353	B1	1996-11-26	Parmenter et al.	Entire Document
41	5696952	B1	1997-12-09	Pontarelli	Entire Document

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42	5774704	B1	1998-06-30	Williams	Entire Document
43	5986479	B1	1999-11-16	Mohan, Jitendra	Entire Document
44	6603348	B1	2003-08-05	Preuss et al.	Entire Document

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U.S.PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20040153802	A1	2004-08-05	Kudo et al.	Entire Document
	2	20020059543	A1	2002-05-16	Cheng et al.	Entire Document
	3	20010010083	A1	2001-07-26	Satoh	Entire Document
	4	20020133794	A1	2002-09-19	Kanapathippillai et al.	Entire Document
	5	20020065646	A1	2002-05-30	Waldie et al.	Entire Document
	6	20020133771	A1	2002-09-19	Barnett	Entire Document

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	1	USPTO Notice of Allowance for Application Number 09/875,599 (CD00180) dated 10/17/2006; 4 pages	<input type="checkbox"/>
	2	USPTO Non-Final Rejection for Application Number 09/875,599 (CD00180) dated 05/31/2006; 18 pages	<input type="checkbox"/>
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45	USPTO Non-Final Rejection for Application Number 09/923,461 (CD00192) dated 07/16/2004; 6 pages	<input type="checkbox"/>
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47	USPTO Notice of Allowance for Application Number 09/929,891 (CD00226) dated 06/15/2005; 6 pages	<input type="checkbox"/>
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49	USPTO Notice of Allowance for Application Number 10/803,030 (CD00169C) dated 01/08/2007; 6 pages	<input type="checkbox"/>
50	USPTO Non-Final Rejection for Application Number 10/803,030 (CD00169C) dated 06/08/2005; 4 pages	<input type="checkbox"/>

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	1	4608502	B1	1986-08-26	Dijkmans et al.	Entire Document
	2	6373954	B1	2002-04-16	Malcolm et al.	Entire Document
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	4	6850117	B1	2005-02-01	Weber et al.	Entire Document
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	6	5248843	B1	1993-09-28	Billings	Entire Document
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28	USPTO Non-Final Rejection for Application Number 10/305,589 (CD02181) dated 10/07/2003; 6 pages	<input type="checkbox"/>
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30	Application Number 09/849,164 (CD01035): "Reduced Static Phase Error CMOS PLL Charge Pump," Stiff; 30 pages	<input type="checkbox"/>
31	Maneatis, "Low-Jitter Process-Independent DLL and PLL Based on Self-Biased Techniques," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996; 10 pages	<input type="checkbox"/>
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33	USPTO Notice of Allowance for Application Number 10/327,217 (CD02174) dated 08/12/2004; 6 pages	<input type="checkbox"/>
34	USPTO Non-Final Rejection for Application Number 10/327,217 (CD02174) dated 04/30/2004; 5 pages	<input type="checkbox"/>
35	USPTO Miscellaneous Action for Application Number 10/327,217 (CD02174) dated 02/10/2004; 1 page	<input type="checkbox"/>
36	Application Number 10/327,217 (CD02174): "Single Ended Clock Signal Generator Having a Differential Output," Richmond et al.; 27 pages	<input type="checkbox"/>
37	USPTO Notice of Allowance for Application Number 10/871,582 (CD03036) dated 03/30/2006; 6 pages	<input type="checkbox"/>
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Attorney Docket Number	CD01174M

39	USPTO Non-Final Rejection for Application Number 10/871,582 (CD03036) dated 09/07/2005; 7 pages	<input type="checkbox"/>
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	2	6560306	B1	2003-05-06	Duffy et al.	Entire Document
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	1	Application Number 10/226,911 (CD02112): "Calibration of Integrated Circuit Time Constants," Gehring et al.; 32 pages	<input type="checkbox"/>
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6	Application Number 09/943,149 (CD01085): "Method for Phase Locking in a Phase Lock Loop," Moyal et al.; 21 pages	<input type="checkbox"/>
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10	Application Number 09/047,595 (CD97115): "Roving Range Control to Limit Receive PLL Frequency of Operation," Scott; 35 pages	<input type="checkbox"/>
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17	Application Number 09/608,753 (CD00018): "PLL Lockout Watchdog," Wilson et al.; 24 pages	<input type="checkbox"/>
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	4	Application Number 11/200,619 (CD04112): "Providing hardware independence to automate code generation of processing device firmware," Snyder et al.; 41 pages	<input type="checkbox"/>
	5	USPTO Notice of Allowance for Application Number 11/201,922 (CD04111) dated 04/09/2009; 4 pages	<input type="checkbox"/>

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Art Unit	2178
Examiner Name	Kyle R. Stork
Attorney Docket Number	CD01174M

6	USPTO Non-Final Rejection for Application Number 11/201,922 (CD04111) dated 10/21/2008; 12 pages	<input type="checkbox"/>
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8	USPTO Non-Final Rejection for Application Number 11/201,922 (CD04111) dated 10/15/2007; 10 pages	<input type="checkbox"/>
9	Application Number 11/201,922 (CD04111): "Design model for a hardware device-independent method of defining embedded firmware for programmable systems," McDonald et al.; 31 pages	<input type="checkbox"/>
10	USPTO Non-Final Rejection for Application Number 11/201,627 (CD04109) dated 12/12/2008; 17 pages	<input type="checkbox"/>
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12	USPTO Non-Final Rejection for Application Number 11/201,627 (CD04109) dated 11/16/2007; 16 pages	<input type="checkbox"/>
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14	USPTO Notice of Allowance for Application Number 09/989,808 (CD01169M) dated 02/13/2006; 4 pages	<input type="checkbox"/>
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16	USPTO Non-Final Rejection for Application Number 09/989,808 (CD01169M) dated 04/14/2005; 8 pages	<input type="checkbox"/>

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17	Application Number 09/989,808 (CD01169M): "Automatic generation of application program interfaces, source code, interrupts, and data sheets for microcontroller programming," Bartz et al.; 67 pages	<input type="checkbox"/>
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20	Application Number 10/109,979 (CD01211M): "Graphical user interface with logic unifying functions," Anderson et al.; 100 pages	<input type="checkbox"/>
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25	USPTO Miscellaneous Action for Application Number 09/989,819 (CD01181M) dated 12/14/2001; 1 page	<input type="checkbox"/>
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28	USPTO Final Rejection for Application Number 09/989,761 (CD01179M) dated 08/26/2004; 7 pages	<input type="checkbox"/>
29	USPTO Non-Final Rejection for Application Number 09/989,761 (CD01179M) dated 03/10/2004; 6 pages	<input type="checkbox"/>
30	USPTO Final Rejection for Application Number 09/989,761 (CD01179M) dated 10/03/2003; 7 pages	<input type="checkbox"/>
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39	PCT International Search Report of the International Searching Authority for PCT/US05/28793, filed 08/12/2005, mailed 11/19/2007; 5 pages	<input type="checkbox"/>
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41	Burogs et al., "Power Converter Analysis and Design using Matlab: A Transfer Function Approach," Proceedings of IEEE International Symposium on Industrial Electronics 1998, Vol. 2; 6 pages	<input type="checkbox"/>
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	1	5708798	B1	1998-01-13	Lynch et al.	Entire Document
	2	6825869	B1	2004-11-30	Bang	Entire Document
	3	4942540	B1	1990-07-17	Black et al.	Entire Document
	4	5394522	B1	1995-02-28	Sanchez-Frank et al.	Entire Document
	5	5867399	B1	1999-02-02	Rostoker et al.	Entire Document
	6	5930150	B1	1999-07-27	Cohen et al.	Entire Document
	7	5933356	B1	1999-08-03	Rostoker et al.	Entire Document
	8	5941991	B1	1999-08-24	Kageshima	Entire Document

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9	6246410	B1	2001-06-12	Bergeron et al.	Entire Document
10	6366300	B1	2002-04-02	Ohara et al.	Entire Document
11	6388109	B1	2002-05-14	Shwarz et al.	Entire Document
12	6457479	B1	2002-10-01	Zhuang et al.	Entire Document
13	6571331	B1	2003-05-27	Henry et al.	Entire Document
14	6594799	B1	2003-07-15	Chiang et al.	Entire Document
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	2	Ebeling et al., "Validating VLSI Circuit Layout by Wirelist Comparison;" Sept. 1983; in proceedings of the IEEE International Conference on Computer Aided Design (ICCAD-83); 2 pages	<input type="checkbox"/>
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	2	7323879	B1	2008-01-29	Kuo et al.	Entire Document
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	4	6280391	B1	2001-08-28	Olson et al.	Entire Document
	5	6326859	B1	2001-12-04	Goldman et al.	Entire Document
	6	6714817	B1	2004-03-30	Daynes et al.	Entire Document
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17	Application Number 11/709,866 (CD05170): "Input/Output Multiplexer Bus," Dennis Sequine; 33 pages	<input type="checkbox"/>
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20	Sedra et al., "Microelectronic Circuits," 3rd Edition, Oxford University Press; 20 pages	<input type="checkbox"/>
21	Van Ess, David; "Simulating a 555 Timer with PSoC," Cypress Semiconductor Corporation, Application Note AN2286, May 19, 2005; 10 pages	<input type="checkbox"/>
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